

ABSTRACT

A LVDS output driver has been disclosed. One embodiment of the LVDS output driver includes a number of source followers, each of the source followers including a pull-down transistor having a source, a drain, a gate, and a bulk terminal. The embodiment of the LVDS output driver further includes a number of pull-up transistors, each of the pull-up transistors having a source, a drain, and a gate, wherein the drain of each of the pull-up transistors is coupled to the source of a pull-down transistor of the source followers, to output a number of differential signals via the drains of the pull-up transistors. Other embodiments are described and claimed.